

Fig. 1

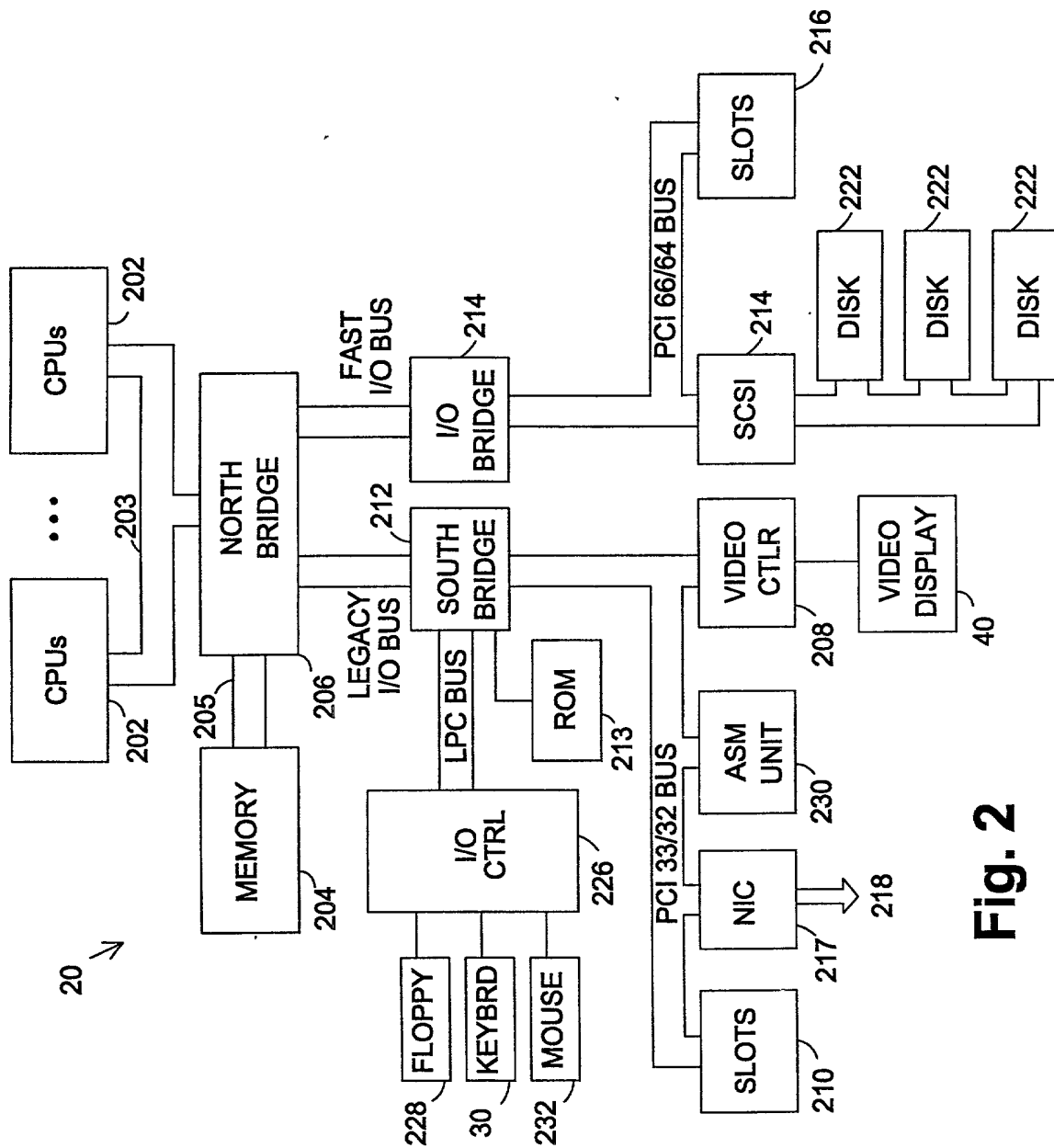


Fig. 2

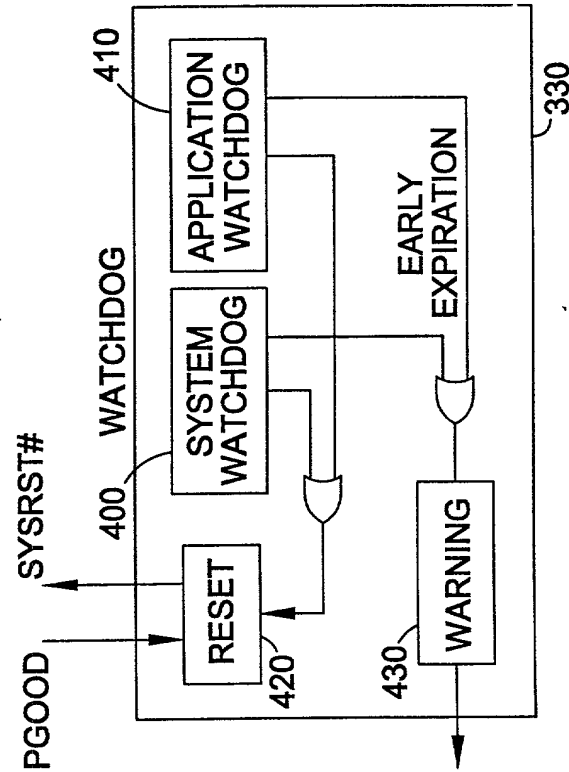


Fig. 4

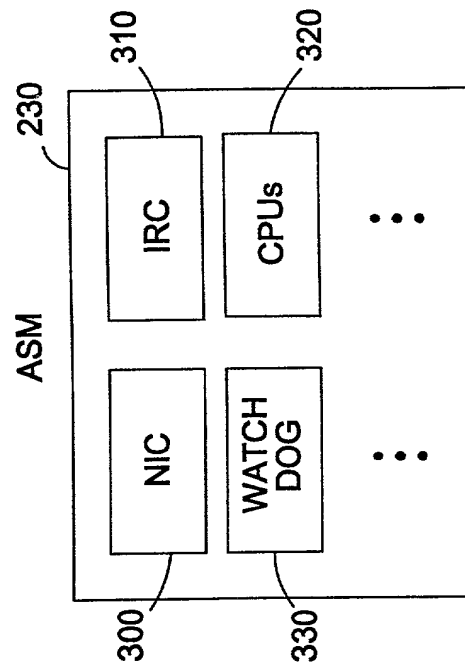


Fig. 3

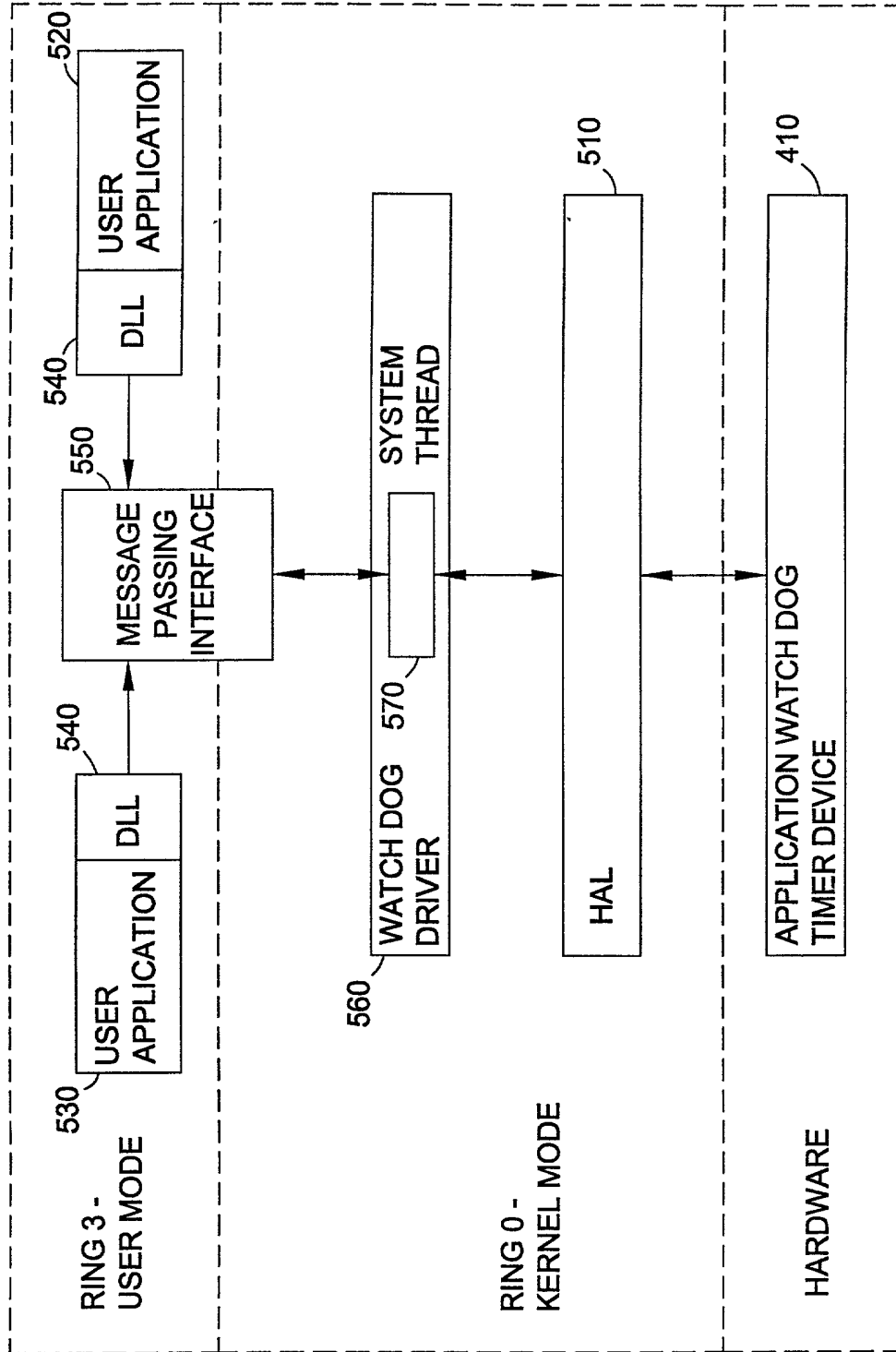


Fig. 5

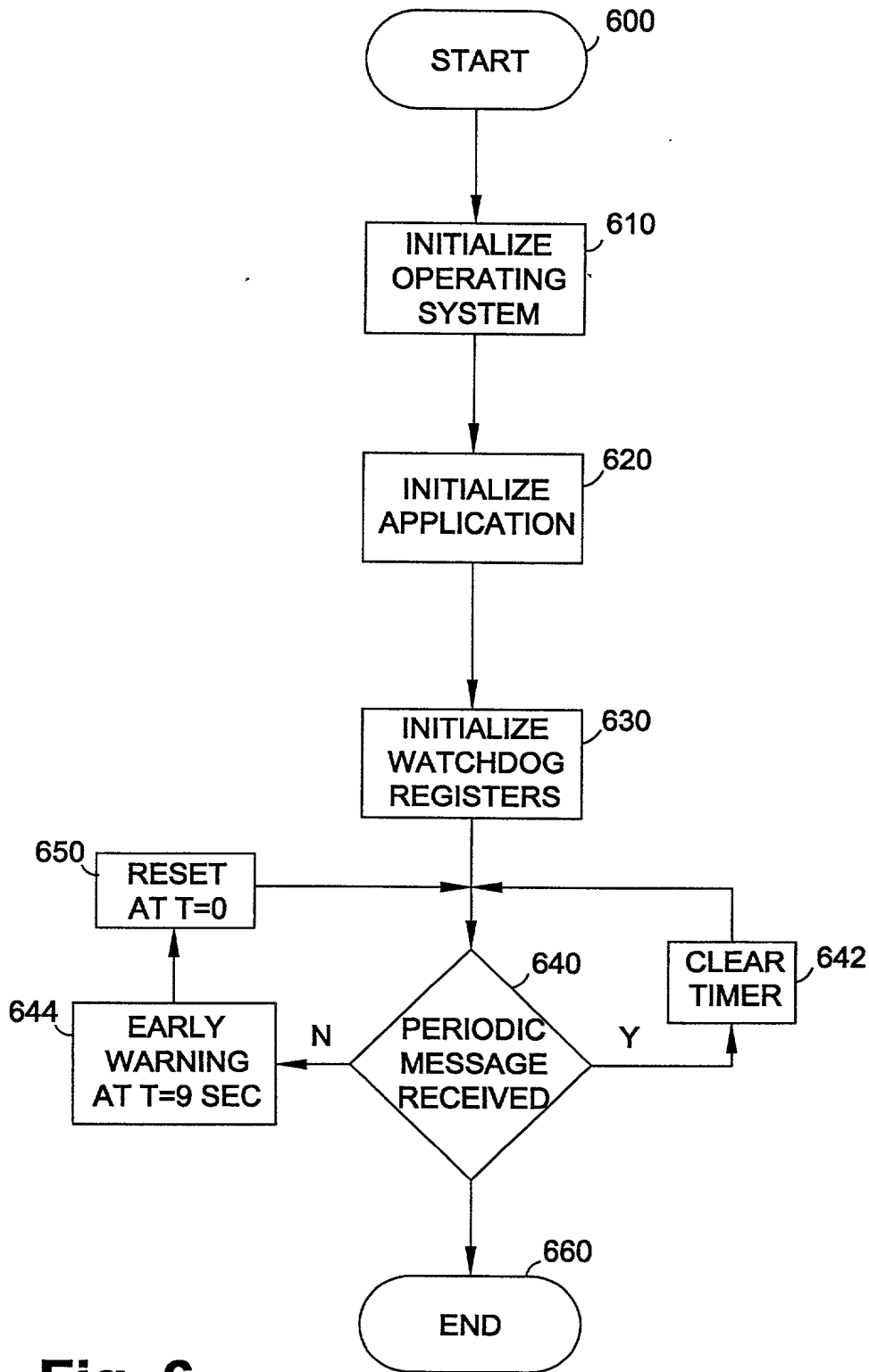


Fig. 6

APPLICATION WATCHDOG
TIMER VALUE REGISTER

700

LOC	BIT	READ/WRITE	RESET	DESCRIPTION
15:0	COUNT	R/W	0	TIMER CLEAR VALUE

APPLICATION WATCHDOG TIMER
CONTROL/STATUS REGISTER

710

LOC	BIT	READ/WRITE	RESET	DESCRIPTION
7	NMISTAT	R/W	0	Warning NMI Status
6	SMISTAT	R/W	0	Warning SMI Status
5:4	RESERVED	R	0	RESERVED
3	SMIEN	R/W	0	Warning SMI Enable
2	NMIEN	R/W	0	Warning NMI Enable
1	RELOAD	W	0	Timer Clear
0	ENABLE	R/W	0	Count Enable

Fig. 7